AMENDMENTS TO THE CLAIMS

Claims 1 – 71 (cancelled).

Claim 72.   (currently amended)   A method for writing data to a memory device, comprising:

issuing a write command to the memory device;

issuing input data to the memory device; and

issuing a control signal to the memory device;

wherein

when said control signal is in a first state the memory device is caused to perform a write operation while posting said input data <u>by storing said input data in a temporary storage area, transferring said input data from said temporary storage area to a write latch, and writing said input data from said write latch to an array of said memory device when said array is available for access,</u> and

when said control signal is in a second state the memory device is caused to perform a write operation without posting said input data.

Claim 73.   (original)   The method of claim 72, wherein said control signal is set to said second state if a next command following said write command is another write command.

Claim 74.   (original)   The method of claim 72, wherein if said control signal is set to said first state if a next command following said write command is a read command.

Claim 75.   (cancel)

Claim 76.   (currently amended)   A method for writing a input data to a memory device, comprising:

determining a next command following a write command;

if said next command is a read command, causing said memory device to perform a write while posting said input data; and

if said next command is not a read command, causing said memory device to perform a write without posting said input data;[[.]]

wherein said memory device includes a write latch and a buffer for storing said input data when said input data is posted, and said input data is transferred to a write latch and subsequently written to an array of said memory device when said array is available for writing.

Claim 77.   (original)   The method of claim 76, wherein said causing said memory device to perform a write while posting said input data comprises:

issuing to the memory device, a posted write command.

Claim 78.   (currently amended)   The method of claim 76, wherein said causing said memory device to perform a write while posting said input data comprises:

issuing a write command to the memory device; and

issuing a control signal to the memory device;

wherein said control signal is set to a first state.

Claim 79.   (original)   The method of claim 76, wherein said causing said memory device to perform a write without posting said input data comprises:

issuing to the memory device, a write command.

Claim 80.   (original)   The method of claim 79, wherein said causing said memory device to perform a write without posting said input data further comprises:

issuing a control signal to the memory device;

wherein said control signal is set to a second state.

Claim 81.   (currently amended)   A method for operating a memory device, comprising:

receiving a write command;

receiving an input data associated with said write command;

receiving a control signal associated with said write command;

determining, based on a state of said control signal, whether to process said write command by writing said input data to an array of said memory device, or by posting said input data to a temporary storage area before writing said input data to said array of said memory device;

wherein said writing said input data to said array of said memory device comprises:

transferring said input data to a write latch associated with said array; and

writing said input data from said write latch to said array when said array is available for writing.

determining whether to process said write command by directly writing said input data to an array of said memory device, or by posting said input data to a temporary storage area before writing said input data to the array based on a state of said control signal.

Claim 82.   (currently amended)   The method of claim 81, wherein when said input data is posted to the temporary storage area, an address associated with said

input data is written to a second temporary storage area and said writing to the array is performed a_n address of said array corresponding to said address.

Claim 83.   (original)   The method of claim 81, further comprising:

writing input data previously stored in said temporary storage area to said array when a set of input/output gates associated with said array are not being used.

Claim 84.   (currently amended) A method for operating a memory device, comprising:

receiving a write command and an ~~associated~~ input data;

determining whether said write command is an ordinary write command or a posted write command;

if said write command is an ordinary write command, processing said write command by immediately ~~directly~~ writing said ~~associated~~ input data to an array of said memory device; and

if said write command is a posted write command, processing said write command by writing said ~~associated~~ input data to a temporary storage area before writing said ~~associated~~ input data from s̲aid temporary storage area to an array of said memory device;[[.]]

wherein said writing from said temporary storage area to said array comprises:

transferring said input data from said temporary storage area to a write latch associated with said array; and

writing said input data from said write latch to said array when said array is available for writing

Claims 85-86. (cancel)